

AF Zhu

In re the Application of: OHASHI, Hitoshi et al.

Group Art Unit: 1762

Serial No.: 10/047,992

Examiner: Brian K. TALBOT

Filed: January 17, 2002

P.T.O. Confirmation No.: 5363

For: METHOD AND APPARATUS FOR FORMING

ELECTRIC CIRCUIT FIXING THE SEMICONDUCTOR

DEVICE USING SUBSTRATE JIG

AMENDMENT UNDER 37 CFR §1.116 - EXPEDITED RESPONSE GROUP ART UNIT 1762

MAIL STOP AF

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

December 21, 2006

Sir:

In response to the Final Office Action dated **September 26, 2006**, please amend the above-identified application as follows: